

IN THE CLAIMS:

Please amend the claims as follows:

In claim 2, line 1, change "a" to --the--;

In claim 2, line 2, change "timing" to --clock--;

In claim 6, line 1, change "a" to --the--;

In claim 9, line 2, delete "local";

In claim 10, line 1, change "an" to --the--;

In claim 10, line 2, change "an" to --the--; and change "a" at the end of the line to --the--;

and

In claim 10, line 3, change "a" to --the--.

14. (Amended) The method of claim 13, wherein generating [a] the timing signal in response to the at least one twice delayed clock signal comprises:
generating a rising edge of the timing signal in response to the delayed clock signal delayed by
[a] the fixed number of delays; and
generating a falling edge of the timing signal in response to the inverse of the delayed clock signal delayed by [a] the variable number of delays.

In claim 19, line 2, delete ", the circuitry";

In claim 23, line 10, after "signal," delete "the"; and

In claim 24, line 5, after "signal," delete "the".

IN THE DRAWINGS:

Accompanying this Preliminary Amendment is a Letter to the Chief Draftsman submitting proposed revisions to FIGS. 3 and 4. Approval of the revised figures is respectfully requested.